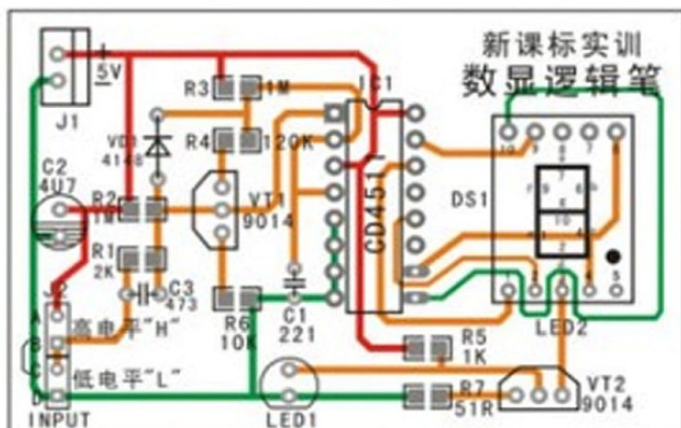
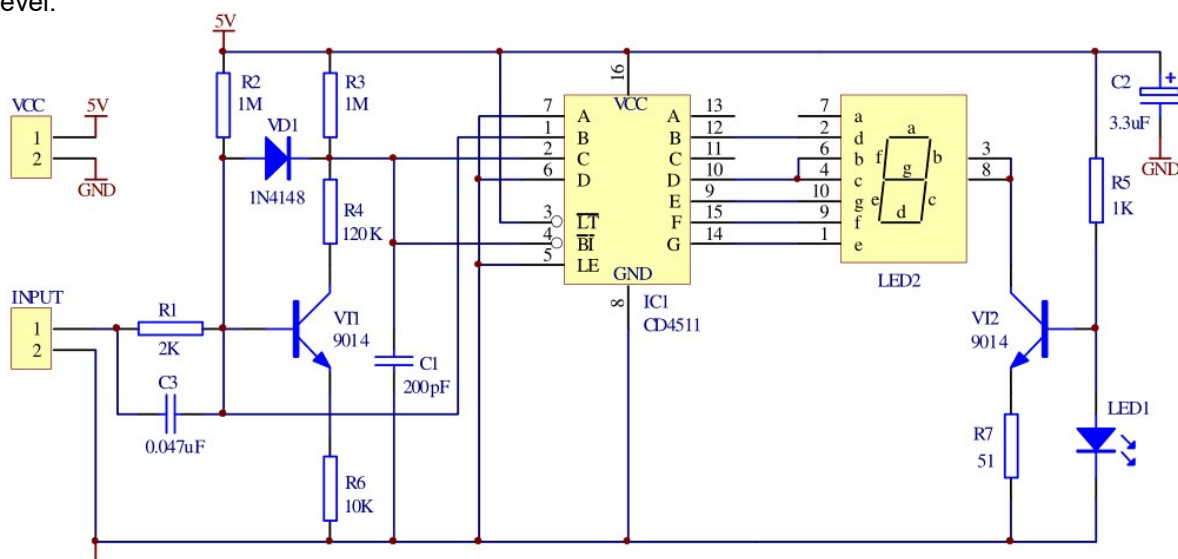


## Operating Introduction

When INPUT port hasn't input voltage, that is when A-B/C-D both disconnect, VIN is connected, CD4511 2-pin, 4-pin are low level since IC1 the fourth pin is blanking input control port; when BI=0, no matter what other input ports' state are, its output port A-G are all 0; and 7 segment digital tube are all off and not display number.

When INPUT port inputs low level which is lower than 0.47V, that is short circuit INPUT C-D port, VT1 cut off, and its collector output high level; IC1 BCD code input DCBA=0100, after decoding, IC1 output port B/C/F/E is high level, and others are low level. So digital tube display "L" representing logic low level.

INPUT port input 3.13-5V high level, that is short circuit INPUT A-B port, VT1 is connected; its base and collector are both high level, IC1 BCD code input DCBA=0110; after decoding, IC1 output port A/C/D/E/F/G output high level; and others are low level; digital tube display "H" representing logic high level.



Label	Name of the	Specifications	Number of	Label	Name of the	Specifications	Number of
R1	Resistance	2K	1	C1	Ceramics capacitors	221	1
R2/R3	Resistance	1M	2	C2	Electrolytic capacitor	10uF	1
R4	Resistance	120K	1	C3	Ceramics capacitors	473	1
R5	Resistance	1K	1	LED1	Light-emitting diode	5 mm red	1
R6	Resistance	10K	1	LED2	1 bit digital tube	A total of Yin 0.56	1
R7	Resistance	51	1	INPUT	Single needle	4P	1
VT1/VT2	Triode	9014	2	U1	integrated circuit	CD4511	1
VD1	Diode	1N4148	1		Short circuit cap		1
	PCB		1		Conductor		2